AMENDMENT TO CLAIMS

Please **AMEND** claim 21 as follows.

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Previously Presented) A flat panel display, comprising:

a plurality of pixels, each of the plurality of pixels including R, G and B unit pixels to embody red (R), green (G) and blue (B) colors, respectively, each of unit pixels including a transistor with source/drain regions,

wherein transistors of at least two unit pixels of the R, G and B unit pixels have drain regions of different geometric structures, and

wherein the drain regions of the transistors of the R, G and B unit pixels are of zigzag shapes.

2 – 4. (Canceled)

5. (Previously Presented) The flat panel display according to claim 1, wherein each unit pixel further includes a light emitting device driven by the transistor, and the drain region of a transistor to drive the light emitting device having the highest luminous efficiency of the light emitting devices among the transistors in the unit pixels has a longer length compared with lengths of drain regions of transistors to drive light emitting devices having a relatively lower luminous efficiency.

6. (Original) The flat panel display according to claim 1, wherein the drain regions of the transistors of the R, G and B unit pixels include offset regions having different geometric structures from one another, respectively.

7. (Previously Presented) The flat panel display according to claim 6, wherein each unit pixel further includes a light-emitting device driven by the transistor, and the drain offset region of the transistor to drive the light emitting device having the highest luminous efficiency among the transistors in the unit pixels has a longer length, in comparison with lengths of drain offset regions of transistors to drive light emitting devices having a relatively low luminous efficiency.

8. (Canceled)

9. (Previously Presented) The flat panel display according to claim 6, wherein the drain offset regions of the transistors of the R, G and B unit pixels are of zigzag shapes.

10. (Original) The flat panel display according to claim 1, wherein the unit pixels further include light-emitting devices, respectively, and channel layers of the transistors controlling currents supplied to the light emitting devices of the unit pixels are of same size.

11-20. (Canceled)

21. (Currently Amended) A flat panel display, comprising:

a plurality of pixels, each of the plurality of pixels including R, G and B unit pixels to embody red (R), green (G) and blue (B) colors, respectively, each of unit pixels including a transistor with source/drain regions,

wherein transistors of at least two unit pixels of the R, G and B unit pixels have drain regions of different geometric structures, and

wherein the drain regions of the transistors of the [[B]] R, G and B unit pixels are of a construction having a same width and a different length from one another.

22. (Previously Presented) The flat panel display according to claim 21, wherein each unit pixel further includes a light emitting device driven by the transistor, and the drain region of a transistor to drive the light emitting device having the highest luminous efficiency of the light emitting devices among the transistors in the unit pixels has a longer length compared with lengths of drain regions of transistors to drive light emitting devices having a relatively lower luminous efficiency.

23. (Previously Presented) The flat panel display according to claim 21, wherein the drain regions of the transistors of the R, G and B unit pixels include offset regions having different geometric structures from one another, respectively.

24. (Previously Presented) The flat panel display according to claim 23, wherein each unit pixel further includes a light-emitting device driven by the transistor, and the drain offset region of the transistor to drive the light emitting device having the highest luminous efficiency among the transistors in the unit pixels has a longer length in comparison with lengths of drain offset regions of transistors to drive light emitting devices having a relatively low luminous efficiency.

25. (Previously Presented) The flat panel display according to claim 23, wherein the drain regions of the transistors of the R, G and B unit pixels include offset regions having a same width and a different length from one another.

26. (Previously Presented) The flat panel display according to claim 21, wherein the unit pixels further include light-emitting devices, respectively, and channel layers of the transistors controlling currents supplied to the light emitting devices of the unit pixels are of same size.

27. (Previously Presented) A flat panel display, comprising:

a plurality of pixels, each of the plurality of pixels including R, G and B unit pixels to embody red (R), green (G) and blue (B) colors, respectively, each of unit pixels including a transistor with source/drain regions,

wherein transistors of at least two unit pixels of the R, G and B unit pixels have drain regions of different geometric structures, and

wherein the drain regions of the transistors of the R, G and B unit pixels include offset regions having a same length and a different width from one another.

28. (Previously Presented) The flat panel display according to claim 27, wherein each unit pixel further includes a light-emitting device driven by the transistor, and the drain offset region of the transistor to drive the light emitting device having the highest luminous efficiency among the transistors in the unit pixels has a narrower width in comparison with widths of drain offset regions of transistors to drive light emitting devices having a relatively low luminous efficiency.

29. (Previously Presented) The flat panel display according to claim 27, wherein the unit pixels further include light-emitting devices, respectively, and channel layers of the transistors controlling currents supplied to the light emitting devices of the unit pixels are of same size.